

Application No. 10/729,275  
Response dated April 23, 2007  
Reply to Office action of March 27, 2007

## **REMARKS**

The Examiner and the undersigned Patent Agent discussed the March 27, 2007 Advisory Action during a phone conversation on April 17, 2007. With respect to pending claims 1-8 and 17-24, the Examiner agreed that the current amendments and remarks would place these claims in condition for allowance.

### ***Amendment to Claim 1 and Claim 2***

In claim 1 and claim 2, "machine-readable" memory was deleted, so that the claims now just recite a memory.

### ***Support for Claim 17***

The "first circuit" and "second circuit" are supported, for example, in paragraphs [0024]:

The methods described above may be implemented using **one or more data processing devices**. In some embodiments, the data processing devices may implement the functionality of the present invention in hardware, using, for example, a computer chip. The data processing device may receive signals in analog or digital form. In other embodiments, the data processing device may implement the functionality of the present invention as software on a general purpose computer, video display device, or other electronic device. In such an embodiment, the program may be written in any one of a number of programming languages, such as FORTRAN, PASCAL, C, C++, C#, Tcl, or BASIC. Further, the program can be written in a script, macro, or functionality embedded in commercially available software, such as EXCEL or VISUAL BASIC.

### ***Claim Rejections 35 USC §103***

Claims 1-8 and 17-24 were rejected under 35 USC §103(a) as being unpatentable over US Patent Application Publication, Pub., No. US 2004/0100577 (**LINZER et al**) in view of US Patent Application Publication, Pub., No. US 2003/0095126 (**Dean**). The Applicant disagrees and traverses the Examiner's rejections.

The Examiner correctly states, "**LINZER et al** does not teach three colorspace components and one type of colorspace component is stored in each burst." In **LINZER et al** a stated goal is to "have fewer bursts because lines are stored together." [**LINZER et al**; paragraph 0008]. Therefore, it is incorrect for the Examiner to state, "[i]t would have been made obvious to one of ordinary skilled in the art at the time the invention was made to incorporate the teachings of **Dean** into **LINZER et al** to have a burst comprising color space components of a single type."

**LINZER et al** teaches away from "one type of colorspace component ... stored in each burst." It is a fundamental starting point of **LINZER et al** that "lines are stored together." A line comprises pixels; pixels comprise colorspace components; and "**LINZER et al** does not teach three colorspace components and one type of colorspace component is stored in each burst." It follows that the goal in **LINZER et al** to "have fewer bursts" when "lines are stored together" can not be achieved if there is "a burst comprising color space components of a single type" as suggested by the Examiner. With the Examiner's combination of **LINZER et al** and **Dean**, reading a pixel according **LINZER et al** would require accessing Three (3) bursts rather than One (1) burst. The combination created by the Examiner would not succeed in accomplishing the goal of **LINZER et al**. Therefore, the "teaching or suggestion to make the claimed combination and the reasonable expectation of success" can not be "found in the prior art."

Furthermore, **Dean** teaches two steps of storing, whereas claims 1-8 and 17-24 require only one step of storing. For example, paragraph [0014] in **Dean** describes "a source processing system"; "an intermediate processing system"; and "a pair of storage queues ... to temporally store data." In paragraph [0015], **Dean** states "the present invention describes a system and method that can efficiently burst sets of color specific video data from a storage queue to a shared memory." Claims 1-8 and 17-24 do not utilize the two step method in **Dean**; do not claim an "intermediate processing system"; and do not require a "storage queue."

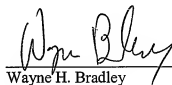
Application No. 10/729,275  
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Therefore, claims 1-8 and 17-24 are not obvious in light of methods and systems which result from **Dean and LINZER et al.** The Applicants believe that the cited references, whether they are taken alone or combined in any way, do not anticipate, teach, disclose or suggest the claimed invention as presented.

In view of at least the foregoing, it is respectfully submitted that the pending claims 1-8 and 17-24 are in condition for allowance. A Notice of Allowability is courteously solicited for claims 1-8 and 17-24. Should the Examiner disagree or have any questions regarding this submission, the Applicant respectfully requests that the Examiner telephone the undersigned at (312) 775-8000. The Commissioner is hereby authorized to charge additional fee(s) or credit overpayment(s) to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

Respectfully submitted,

Date: April 23, 2007



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